

**low-power, high-speed cmos analog switches** - vishay siliconix dg401, dg403, dg405 document number: 70049 s09-2561-rev. i, 30-nov-09 vishay 1 low-power, high-speed cmos analog switches features **hcmos design considerations (rev. a) - ti** - 1 introduction hcmos data sheets specify, under recommended operating conditions, input  $t_t = 1000$  ns, (10%  $\rightarrow$  90%) for  $v_{cc} = 2$  v. if certain devices are used in the threshold region (from  $v_{ilmax} = 0.5$  v to  $v_{ihmin} = 1.5$  v), there is a potential to go into the wrong state from induced grounding, causing double clocking. **ucc2742x dual 4-a high speed low-side mosfet drivers with ...** - 8 7 6 5 1 2 3 4 gnd gnd gnd ucc2742x enba enbb ina inb v+ enba ina gnd inb enbb outa vdd outb product folder sample & buy technical documents tools & software support ... **low drop-out (ldo) linear regulators: design ...** -  $\phi$  nmos pass fet is easier to compensate at low loads and dropout, due to the higher output impedance of pmos.  $\phi$  nmos pass fet are smaller due to weaker drive of pmos. **16-ch/dual 8-ch high-performance cmos analog multiplexers** - dg406, dg407 vishay vishay siliconix s13-2518-rev. k, 09-dec-13 1 document number: 70061 for technical questions, contact: analogswitchsupport@vishay this document is subject to change without notice. **interface products design guide using can, lin and ...** - interface products design guide using can, lin and infrared connectivity analog and interface product solutions lin transceiver mcp201 general purpose i/o expander **application circuit examples - hamamatsu photonics** - 43 si photodiodes +-+15 v pd-15 v vo 4 6 10 k ic 1 k +15 v 3 2 7 14 2 sw 1 1 k c 13 7 reset input vo reset input isc t t to t isc reset input: use ttl  $\phi$  to reset. ic : lf356, etc.

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