

the performance of circuit switching in the internet - switching in shared access or local area networks. but our results suggest that in the core of the network, where high capacity is needed most, and where peak ow rate is limited by the access link, there is little or no di erence in performance between circuit switching and packet switching. given that circuit switches can be built to be **vol. no. 1988 queueing in high-performance packet switching** - queueing in high-performance packet switching abstract-because of the unscheduled nature of arrivals to a packet switch, two or more packets may arrive on different inputs destined for the same output. the switch architecture may allow one of these packets to pass through to the output, but the others must be queued for later transmission. **bandwidth problem in high performance packet switching network** - the deployment of high performance packet switching networks is hindered due to the improper congestion control which consequently results longer delays. in this paper, we propose a new method that can effectively improve the congestion control in high performance packet switching networks. our **gigaswitch system: a high-performance packet-switching ...** - gigaswitch system: a high-performance packet-switching platform by robert j. souza, p. g. krishnakumar, cÃfÃ½neyt m. ÃfÃ½zveren, robert j. simcoe, barry a. spinney, robert e. thomas, robert j. walsh abstract the gigaswitch system is a high-performance packet-switching platform built on a 36-port 100 mb/s crossbar switching fabric. **the pipeback switch: high performance packet switching ...** - the pipeback switch: high performance packet switching with guaranteed delivery and linear buffer complexity saqib raza and zartash afzal uzmi computer science department lahore university of management sciences, pakistan {saqibr,zartash}@lums abstractÃ¢Ã¢”a high performance packet switching archi-ecture called the pipeback switch is ... **performance analysis of packet switching interconnection ...** - performance analysis of packet switching interconnection networks with finite buffers aristotel tentov and aksenti gnrarov ... and for the high speed packet-switching computer communications in parallel computers [I]. in all these applications, min are a critical part of ... robertazzi [131 present analysis of the performance of a packet ... **performance study of packet switching multistage ...** - performance study of packet switching multistage interconnection networks jungsun kim contents i. introduction ii. the model and the environment ... high-bandwidth communication between processors and/or ... to evaluate the performance of several packet switching mins, we will use the fol-lowingmetrics. lett bea relativelyargetime **overview of power packet switching architecture** - overview of power packet switching architecture ideal powerÃ¢Ã¢”s power packet switching ... efficiency and performance of semiconductor power switch components can have wide economic ... external isolation transformer. additionally, ppsa delivers high-efficiency, near-unity power factor, and low output harmonics. **high- performance ethernet ethernet switch series switching** - high- performance ethernet switching highlights Ã¢Ã¢” compact 1u ip/mppls/vrf-enabled ... with high performance and availability. the switches provide a broad set of advanced layer 2, ipv4, ipv6, and mpls ... speed performance, deep packet buffers, and low latency in a compact 1u form factor. comprehensive support for ipv4 **a hybrid optical packet and wavelength selective switching ...** - hybrid optical packet and wavelength selective switching platform for high-performance data center networks. this architecture based on cascaded silicon microrings and semiconductor optical amplifiers (soas) supports wavelength reconfigurable packet and circuit switching, and is highly scalable, energy efficient and potentially integratble. **an architecture for high-speed packet switched networks ...** - packet switched networks lies in their inability to provide predictable performance. this dissertation proposes a new architecture for providing predictable high performance in high speed packet switched networks. the architecture combines the advantages of circuit switching and packet switching by providing two services: datagramlj and jlowlj. **the tandem banyan switching fabric: a simple high ...** - the tandem banyan switching fabric: a simple high-performance fast packet switch* fouad a. tobagi and timothy c. kwok department of electrical engineering stanford university stanford, ca 94305-4055 abstract in this paper, we introduce a new space-division switch ar- chitecture based on banyan

interconnection networks, called the **on the feasibility of optical circuit switching for high ...** - performance benefits which we believe can be utilized in high performance computer systems: the optical circuit switching (ocs) network. today's high performance computing systems use packet-switched networks to interconnect system processors. inter-processor messages get broken into packets that are routed through network switches.

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