

**introduction to high-performance computing** - 3 when do we need high performance computing? **case1:** complete a time-consuming operation in less time i am an automotive engineer i need to design a new car that consumes less gasoline i'd rather have the design completed in 6 months than in 2 years **tensor comprehensions:**

**framework-agnostic high-performance ...** - tensor comprehensions: framework-agnostic high-performance machine learning abstractions nicolas vasilache facebook ai research ntv@fb oleksandr zinenko **arxiv:1301.3781v3 [cs] 7 sep 2013** - than a few hundred of millions of words, with a modest dimensionality of the word vectors between 50 - 100. we use recently proposed techniques for measuring the quality of the resulting vector representa- **ti's new c66x fixed- and floating-point dsp core conquers ...** - ti's new c66x fixed- and floating-point dsp core conquers the need for speed november 2010 texas instruments 3 up to eight, floating-point multiplies can run simultaneously on a c66x dsp core. **db2 for i5/os: tuning for performance - omni user** - 11 db2 for i5/os: tuning for performance copyright 2007 - ibm corporation degree parameter values \*none no parallel processing is allowed for database query ... **issn: 2319-5967 iso 9001:2008 certified international ...** - issn: 2319-5967 iso 9001:2008 certified international journal of engineering science and innovative technology (ijesit) volume 2, issue 4, july 2013 **empowering automotive vision with ti s vision accelerationpac** - 4 texas instruments the compute cycles needed for iterative pixel operations. the vector coprocessor has specialized pipelines for accelerating histogram, weighted histogram and lookup tables and supports common computer-vision **optimizing software in c++ - agner** - 4 4. instruction tables: lists of instruction latencies, throughputs and micro-operation breakdowns for intel, amd and via cpus. 5. calling conventions for different c++ compilers and operating systems. **big data in r - columbia university** - changing the limit can use memoryze() to change r's allocation limit. but memory limits are dependent on your configuration if you're running 32-bit r on any os, it'll be 2 or 3gb **measuring performance of exchange traded funds** - measuring performance of exchange traded funds 2 measuring the efficiency of exchange traded funds 2.1 performance or efficiency measurement? 2.1.1 understanding fund picking in active management **ece 1010 ece problem solving i interpolation and 6 curve ...** - ece 1010 ece problem solving i chapter 6: interpolation use matlab's interp1 function to estimate vehicle velocities on the interval [0,5] seconds with resolution of 0.01 sec- **semiconductor times june 2010 / 1 june 2010 focused on ...** - semiconductor times june 2010 / 1 copyright 2010, pinestream communications, inc. 52 pine street, weston, massachusetts 02493 usa tel 781.647.8800 fax 781.647.8825 ... **scientia calculating, programming, and writing** - scientia calculating, programming, and writing james d emery edition: 3/22/2016 contents 1 introduction 10 2 programming editors 12 3 some scientia programming tools 13 **simulation study of fir filter based on matlab - ijsrd** - the rectangular frequency characteristics of hd(ejw), so hd(n) must be an infinite sequence and non-causal. the h(n) of fir filter to be designed is inevitable finite; infinite **for version 3.3.8, 24 may 2018 - fftw** - chapter 1: introduction 1 1 introduction this manual documents version 3.3.8 of fftw, the fastest fourier transform in the west. fftw is a comprehensive collection of fast c routines for computing the discrete fourier **cross-cultural depression recognition from vocal biomarkers** - cross-cultural depression recognition from vocal biomarkers sharifa alghowinem 1;5, roland goecke2, julien epps3, michael wagner2;1, jeffrey cohn4, 1 australian national university, research school of computer science, canberra, australia 2 university of canberra, human-centred technology research centre, canberra, australia 3 university of new south wales, sydney, australia **rakesh nath - cea** - rakesh nath chairperson, central electricity authority & ex officio secretary government of india foreword the objective of national electricity policy is power to all by 2012. **integrated power factor correction (pfc) and sensorless ...** - 2008 microchip technology inc. ds01208a-page 3 an1208 a novel approach for digital implementation of pfc and sensorless foc algorithms figure 2 shows a block diagram of the pfc

and sensorless foc control loops implemented digitally using the dspic dsc device. **comparison of particle sizing methods - cps instruments** - cps cps instruments europe p.o. box 180, nl-4900 ad oosterhout, the netherlands t: +31 (0)162 472478 f: +31 (0)162 421944 e: info@cpsinstruments **vehicle tracking and locking system based on gsm and gps** - 90 vehicle tracking and locking system based on gsm and gps copyright 2013 mecs i.j. intelligent systems and applications, 2013, 09, 86-93 the circuit diagram of ...

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